

Ultra Low Power 5V Boost Converter, 20V Linear Charger and Fuel Gauge All-in-One Solution for TWS Charge Cradle

DESCRIPTION

The ETA9084 is an all-in-one solution for TWS charge cradle. It includes a 20V single cell Li+ battery linear charger, a 5V Boost synchronous converter with true-shutoff function and a Fuel Gauge with LED indication.

The linear charger is fully integrated with constant current (CC) / constant voltage (CV) control module and a charge FET. The integrated low power 5V Boost converter is capable of delivering 0.5A current at 5V output. It can be shut down by pulling the ENBST pin low. The true shut down feature disconnect output from the BAT thereby further decreasing the system standby power. There are 4 LEDs driven by 3 output pins to indicate the charge/discharge status and battery level.

The ETA9084 is ideal for applications such as a TWS charging cradle that requires low standby power and small PCB space.

The ETA9084 is housed in a ESSOP10/DFN3x3-10 package

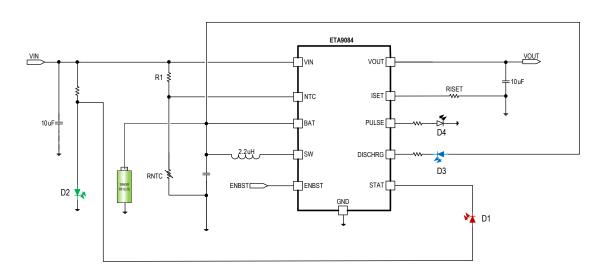
FEATURES

- Ultra low boost standby lq , Ibat<10uA
- 20V Charge input standoff voltage
- 4.2V/4.35V charge termination voltage
- Charge current programmable
- Boost output disconnect and SCP
- 5V/0.5A output power
- Up to 96% Efficiency for boost
- Fuel Gauge and LED indicators
- NTC thermistor input
- Logic Control Shutdown
- Thermal shutdown
- ESSOP10/ DFN3x3-10 Package

APPLICATIONS

- TWS BT earbuds charge case
- Bluetooth application
- Battery powered IOT module
- Power Bank

TYPICAL APPLICATION

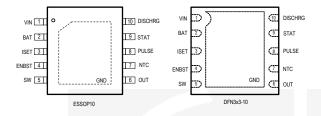




ORDERING INFORMATION

PART No.	PACKAGE	TOP MARK	Pcs/Reel
ETA9084E10	ESSOP10	ETA9084 <u>YWW</u> 2 <u>L</u>	4000
ETA9084D3K	DFN3x3-10	ETA9084 <u>YWW</u> 2 <u>L</u>	5000

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS

ELECTRICAL CHARACTERISTICS

 $(V_{BAT}=3.8V, V_{IN}=5V, V_{OUT}=5V, unless otherwise specified. Typical values are at TA = 25°C.)$

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Linear Charger					
Input Standoff Voltage				20	V
Input Over-Voltage Protection	VIN riging HVC-0.2V		6.8		V
Voltage	VIN rising, HYS=0.3V		0.0		V
Input Voltage Range for Charging		4		6	V
VIN Under-voltage Lockout	VIN rising, HYS=0.5V,		4.5		V
Threshold	enable charging				
	Charge Mode		3		mA
Input Supply Current	Standby Mode		2		m 1
	(Charge Terminated)	2		mA	



PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
	Charge Terminated Voltage	4.16	4.2	4.24	V
Regulated VBATT Voltage	Charge Terminated Voltage, add 30K RES between PULSE and GND pin	4.31	4.35	4.39	V
BAT Pin Current	RISET = 1K, Current Mode		1000		mA
DAI FIII Guileiit	RISET = 2K, Current Mode		500		mA
Trickle Charge Current	VBAT < VTRIKL, RISET = 2K		50		mA
Trickle Charge Threshold Voltage	VBAT Rising		3		V
Trickle Charge Hysteresis Voltage			200		mV
VIN-VBAT Lockout Threshold	VIN from Low to High		100		mV
Voltage	VIN from High to Low		50		mV
Termination Current Threshold	RISET = 2K		50		mA
ISET Pin Voltage	Current Mode, VBAT=4V		1		V
Recharge BAT Threshold Voltage	Vtermination - VRECHRG		150		mV
Thermal regulation When Charging			100		°C
	VIN Voltage start to reduce charging current, when select Vtermination=4.2V		4.45		V
Vhold	VIN Voltage start to reduce charging current, when select Vtermination=4.35V		4.55		V
Power FET "ON" Resistance (Between VIN and BAT)	COL		0.5	NIC	ohm
Soft-Start Time	IBAT = 0 to IBAT =1000V/RISET	UT	200	N.	μs
NTC Threshold, Cold	Charger Suspended		66		% VIN
NTC Threshold, Hot	Charger Suspended		35		% VIN
NTC Threshold Hysteresis			1		% VIN
NTC Input Leakage			0	1	μA
Boost Converter					
Quiescent Current at BAT	Vbat=4V		8	15	μA
Shutdown Supply Current at BAT	V _{ENBST} =GND		3	5	μA
VBAT Startup Voltage	VBAT rising, HYS=0.2V		3.2		V
VBAT Unlock Voltage When boost is lock by low VBAT			3.55		V
LOW BAT WARNING Voltage	HYS=100mV, Rising		3.15		V
Output Voltage at 5V		4.85	5	5.15	V
Switching Frequency			1.0		MHz



PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
NMOS Switch On Resistance	I _{SW} =100mA		100		mΩ
PMOS Switch On Resistance	I _{SW} =100mA		100		mΩ
SW Leakage Current	V_{OUT} =5.2V, V_{ENBST} =GND, V_{SW} =5.2V or V_{SW} = 0V		0	10	μA
NMOS Switch Current Limit			0.9		А
Start-up Current Limit			0.5		А
Chart Circuit History times	ON		24		ms
Short Circuit Hiccup time	OFF		168		ms
ENBST Input Current	V _{ENBST} =5V or 0V	-5	0	5	μA
ENBST High Voltage	V _{OUT} =5V	1.2			V
ENBST low Voltage	V _{OUT} =5V			0.4	V
Thermal Shutdown	Rising, Hysteresis=20°C		140		°C

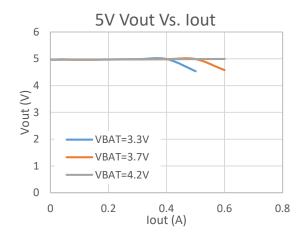
PIN DESCRIPTION

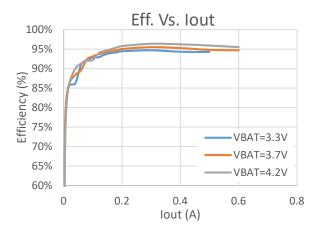
PIN#	NAME	DESCRIPTION
1	VIN	Input Supply Voltage. Bypass with a 10µF ceramic capacitor to GND
2	BAT	Connected to the battery positive terminal. Bypass with a 10uF ceramic capacitor to GND
3	ISET	Charge current program pin. The charge current is programmed by connecting a 1% resistor (RISET), between ISET, to GND pin. The charge current can be calculated by using the following formula: $I_{BAT} = \frac{1}{R_{set}} \times 1000$
4	ENBST	Enable pin for the Boost converter. Leave this pin floating to enable the part, low to disable. Pulling high is not recommended.
5	SW	Inductor Connection. Connect an inductor Between SW and the regulator output.
6	OUT	Output pin. Bypass with a $10\mu F$ or larger ceramic capacitor closely between this pin and GND
7	NTC	Battery Temperature Monitoring input pin. It sets the valid temperature operating range for battery charging. Disable NTC by setting R1=10K, RNTC=10K
8	PULSE	drive a LED is to indicate the battery voltage level, which serves as an 4-level battery "fuel gauge".
9	STAT	drive a LED to indicate the charge status
10	DISCHRG	drive a LED to indicate the discharge status
11	GND	GND

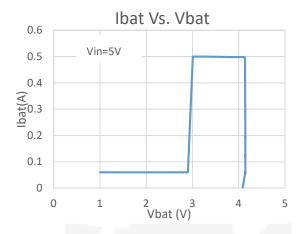


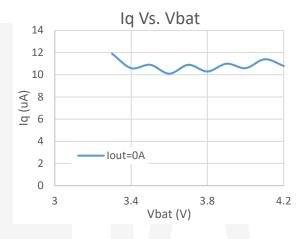
TYPICAL CHARACTERISTICS

(Typical values are at T_A = 25°C unless otherwise specified.)

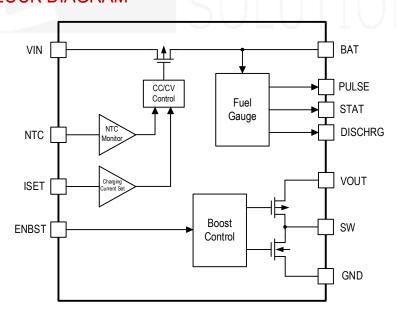








FUNCTION BLOCK DIAGRAM





APPLICATION INFORMATION

ETA9084 is an ultra low power 5V boost converter, 20V linear charger and fuel gauge all-in-one solution for TWS charge cradle, boost converter with true-shutoff function.

Normal Charge Cycle

The ETA9084 initiates a charge cycle once the voltage at the VIN pin rises above the UVLO threshold level. A 1% precision resistor needs to be connected from the ISET pin to ground. If the voltage at the BAT pin is less than 3.0V, the charger enters trickle charge mode. In this mode, the charge current is reduced to nearly 1/10 the programmed value until the battery voltage is raised to a safe level for full current charging.

The charger switches to constant-current mode as the BAT pin voltage rises above 3.0V, the charge current is thus resumed to full programmed value. When the final VBAT voltage (4.2V or 4.35V) is reached, the ETA9084 enters constant-voltage mode and the charge current begins to decrease until it drops to 1/10 of the preset value and ends the charge cycle.

Programming Charge Current

The charge current is programmable by setting the value of a precision resistor connected from the ISET pin to ground. The charge current out of the BAT pin can be using the following equation:

$$I_{BAT} = \frac{1}{R_{set}} \times 1000$$

LED Display Mode

The ETA9084 use D1, D2, D3 LEDS to indicate the charging and discharging status. And a pulse indicator D4 to show the battery voltage level.

Table 1 D1,D2 Charging Status Indicator

<u> </u>				
Ctoto	D1	D2		
State	Red LED	Green LED		
Charging	on	off		
Charging Done	off	on		

Table 2 D3 Discharging Status Indicator

<u> </u>				
State	Battery Voltage Level(C)	D3		
State	Dattery voltage Level(C)	Blue LED		
Dia ah awain n	3% <c<100%< td=""><td>on</td></c<100%<>	on		
Discharging	C≤3%	1Hz flash		
No Discharging		off		



Table 3 D4 Battery Voltage Level Indicator

State	Battery Voltage Level(C)	D4
	0 <c≤25%< td=""><td>flash once in 4s</td></c≤25%<>	flash once in 4s
Charging/Discharging	25% <c≤50%< td=""><td>flash twice in 4s</td></c≤50%<>	flash twice in 4s
	50% <c≤75%< td=""><td>flash three times in 4s</td></c≤75%<>	flash three times in 4s
	75% <c<100%< td=""><td>flash four times in 4s</td></c<100%<>	flash four times in 4s
	Charging Done	on

Charge Termination

The ETA9084 keeps monitoring the ISET pin during the charging process. It terminates the charge cycle when the charge current falls to 1/10 the programmed value after the final VBAT voltage(4.2V or 4.35V) is reached. When the ISET pin voltage falls below 100mV for longer than tTERM (typically 180ms), charging is terminated. The charge current is latched off and the ETA9084 enters standby mode, where the input supply current drops to 2mA. (Note: C/10 termination is disabled in Vhold charging and thermal limiting modes).

During charging, the transient response of the circuit can cause the ISET pin to fall below 100mV temporarily before the battery is fully charged, thus can cause a premature termination of the charge cycle. A 180ms filter time on the termination comparator can prevent this from happening. Once the average charge current drops below 1/10 the programmed value, the ETA9084 terminates the charge cycle and ceases to provide any current through the BAT pin. In this state, all loads on the BAT pin must be supplied by the battery.

The ETA9084 constantly monitors the BAT pin voltage in standby mode and resume another charge cycle if this voltage drops below the recharge threshold. User can also manually restart a charge cycle in standby mode by removing and then reapplied the input voltage

High Temperature Fold-back

Build-in feedback circuitry mechanism can reduce the value of the programmed charge current once the die temperature tends to rise above 100°C, hence prevents the temperature from further increase and ensure device safe operation.

Under-voltage Lockout (UVLO)

Build-in under-voltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until VIN rises above the under-voltage lockout threshold. The UVLO circuit has a built-in hysteresis of 500mV. Furthermore, to protect against reverse current in the power MOSFET, the UVLO circuit keeps the charger in shutdown mode if VIN falls to within 50mV of the battery voltage for 3mS. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until VIN rises 100mV above the battery voltage.



Automatic Recharge

After the termination of the charge cycle, the ETA9084 constantly monitors the BAT pin voltage and starts a new charge cycle when the battery voltage falls below Recharge Voltage, keeping the battery at fully charged condition. ISET pin output enters a strong pull-down state during recharge cycles.

Battery Temperature Monitoring

ETA9084 continuously monitors temperature by measuring the voltage of NTC pin. A negative or positive temperature coefficient thermistor and an external voltage divider typically develop this voltage. ETA9084 compares this voltage against its internal 66%VIN and 35%VIN thresholds to determine if charging is allowed. The temperature sensing circuit is immune to any fluctuation in VIN, since both the external voltage divider and the internal thresholds 66%VIN and 35%VIN are referenced to VIN. If the NTC pin is connected to GND will disable the temperature-sensing feature.

Ultra low current consumption at Light Load Boost Operation

Traditionally, a fixed constant frequency PWM DC/DC regulator always switches even when the output load is small. When energy is shuffling back and forth through the power MOSFETs, power is lost due to the finite RDSONs of the MOSFETs and parasitic capacitances. At light load, this loss is prominent and efficiency is therefore very low. ETA9084 employs a proprietary control scheme that improves efficiency in this situation by enabling the device into a power saving mode during light load and the no load quiescent current can be lower than 10µA.

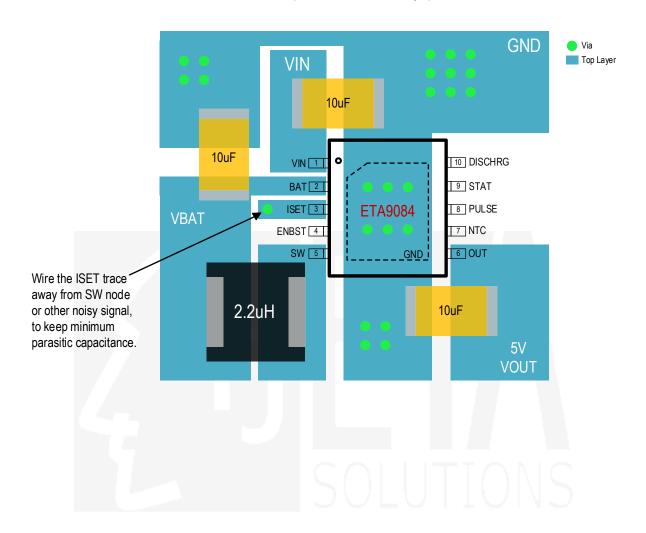
Output (5V OUT pin) Short-Circuit Protection

Unlike most step-up converters, the ETA9084 allows for short circuits on the output. In the event of a short circuit, the device first turns off the NMOS when the sensed current reaches the current limit. When OUT drops below VBAT, the device then enters a linear charge period with the current limited same as with the start-up period. In addition, the thermal shutdown circuits disable switching if the die temperature rises above 140°C.



PCB GUIDELINES

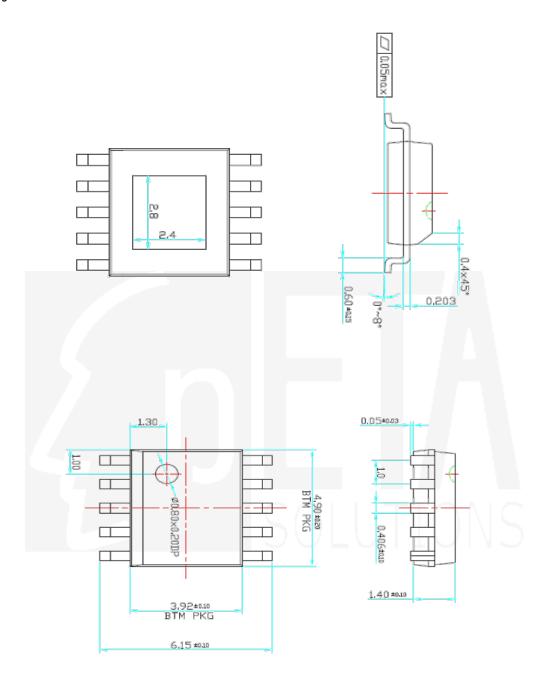
Keep the power devices as close to the chip as possible to achieve the smallest power loop area, which leads to the best EMI performance; Cin is always placed nearest to Vin and GND





PACKAGE OUTLINE

Package: ESSOP10





PACKAGE OUTLINE

Package: DFN3x3-10

